

IN THE CLAIMS:

Please cancel claims 10 and 28 without prejudice or disclaimer.

Claims 1-9, 11, 13-15, 17-23, 25-27, 29, 31-33 and 35-36 are presented below in their amended form. The amendments to the above-noted claims are outlined in an Attachment to the Amendment using the conventional indication method of bracketing and underlining.

*Sub
C*

B2

1. (Amended) An active matrix display device comprising:
 - a substrate having an insulating surface;
 - a plurality of pixel electrodes arranged in a matrix form over said substrate;
 - a plurality of switching elements operationally connected to said pixel electrodes, each of said switching elements comprising a thin film transistor;
 - a display medium capable of electrically changing luminous strength disposed at each of said pixel electrodes; and
 - a driver circuit comprising a plurality of thin film transistors for driving said plurality of switching elements,
wherein each of said plurality of thin film transistors comprises a crystalline semiconductor layer, a gate insulating film adjacent to said crystalline semiconductor layer and a gate electrode adjacent to said gate insulating film.
2. (Amended) The active matrix display device according to claim 1 wherein said gate electrode is located over said semiconductor layer.
3. (Amended) The active matrix display device according to claim 1 wherein all of said plurality of thin film transistors are p-channel transistors.
4. (Amended) The active matrix display device according to claim 1 wherein all of said plurality of thin film transistors are n-channel transistors.
5. (Amended) The active matrix display device according to claim 1 wherein said substrate is a glass substrate.

Sub C1

7. (Amended) An active matrix display device comprising:
a substrate having an insulating surface;
a plurality of pixel electrodes arranged in a matrix form over said substrate;
a plurality of switching elements operationally connected to said pixel electrodes, each of said switching elements comprising a thin film transistor;
a display medium capable of electrically changing luminous strength disposed at each of said pixel electrodes; and
a driver circuit comprising a plurality of thin film transistors for driving said plurality of switching elements, wherein each of said plurality of thin film transistors comprises a crystalline semiconductor layer, a gate insulating film adjacent to said crystalline semiconductor layer and a gate electrode adjacent to said gate insulating film,
wherein said crystalline semiconductor layer has source and drain regions and at least one lightly doped region.

B3

8. (Amended) The active matrix display device according to claim 7 wherein said substrate is a glass substrate.

9. (Amended) The active matrix display device according to claim 7 wherein said source and drain regions and said at least one lightly doped region are doped with phosphorus.

Sub C1

11. (Amended) The active matrix display device according to claim 7 wherein said gate electrode is located over said semiconductor layer.

Sub C1

13. (Amended) An active matrix display device comprising:
a substrate having an insulating surface;
a plurality of pixel electrodes arranged in a matrix form over said substrate;
a plurality of switching elements operationally connected to said pixel electrodes, each of said switching elements comprising a thin film transistor;
a display medium capable of electrically changing luminous strength disposed at each of said pixel electrodes; and

B5

a CMOS circuit comprising at least one n-channel thin film transistor and one p-channel thin film transistor,

wherein each of said n-channel and p-channel thin film transistors comprises a crystalline semiconductor layer, a gate insulating film adjacent to said crystalline semiconductor layer and a gate electrode adjacent to said gate insulating film.

*B5
Pcontd.*

14. (Amended) The active matrix display device according to claim 13 wherein said substrate is a glass substrate.

15. (Amended) The active matrix display device according to claim 13 wherein said gate electrode is located over said semiconductor layer.

*Subs
C1*

17. (Amended) An active matrix display device comprising:

substrate;

a substrate having an insulating surface;

a plurality of pixel electrodes arranged in a matrix form over said substrate;

a plurality of switching elements operationally connected to said pixel electrodes, each of said switching elements comprising a thin film transistor;

a display medium capable of electrically changing luminous strength disposed at each of said pixel electrodes; and

a CMOS circuit comprising at least one n-channel thin film transistor and one p-channel thin film transistor, each of said first and second thin film transistors comprising a crystalline semiconductor layer, a gate insulating film adjacent to said crystalline semiconductor layer and a gate electrode adjacent to said gate insulating film, wherein said crystalline semiconductor layer has source and drain regions and at least one lightly doped region.

18. (Amended) The active matrix display device according to claim 17 wherein said substrate is a glass substrate.

19. (Amended) An active matrix display device comprising:

 a substrate having an insulating surface;

 a plurality of pixel electrodes arranged in a matrix form over said substrate;

 a plurality of switching elements operationally connected to said pixel electrodes, each of said switching elements comprising a thin film transistor;

 a display medium capable of electrically changing luminous strength disposed at each of said pixel electrodes; and

 a driver circuit comprising a plurality of thin film transistors for driving said plurality of switching elements,

 wherein each of the film transistors of said switching elements and said driver circuit comprises a crystalline semiconductor layer, a gate insulating film adjacent to said crystalline semiconductor layer and a gate electrode adjacent to said gate insulating film.

B6 contd.

20. (Amended) The active matrix display device according to claim 19 wherein said gate electrode is located over said semiconductor layer.

21. (Amended) The active matrix display device according to claim 19 wherein all of said plurality of thin film transistors are p-channel transistors.

22. (Amended) The active matrix display device according to claim 19 wherein all of said plurality of thin film transistors are n-channel transistors.

23. (Amended) The active matrix display device according to claim 19 wherein said substrate is a glass substrate.

B7 Sub C

25. (Amended) An active matrix display device comprising:

 a substrate having an insulating surface;

 a plurality of pixel electrodes arranged in a matrix form over said substrate;

a plurality of switching elements operationally connected to said pixel electrodes, each of said switching elements comprising a thin film transistor;

a display medium capable of electrically changing luminous strength disposed at each of said pixel electrodes; and

a driver circuit comprising a plurality of thin film transistors for driving said plurality of switching elements,

wherein each of the thin film transistors of the switching elements and the driver circuit comprises a crystalline semiconductor layer, a gate insulating film adjacent to said crystalline semiconductor layer and a gate electrode adjacent to said gate insulating film,

wherein said crystalline semiconductor layer has source and drain regions and at least one lightly doped region.

B7
Cond.
26. (Amended) The active matrix display device according to claim 25 wherein said substrate is a glass substrate.

27. (Amended) The active matrix display device according to claim 25 wherein said source and drain regions and said at least one lightly doped region are doped with phosphorus.

B8
Sub
C
29. (Amended) The active matrix display device according to claim 25 wherein said gate electrode is located over said semiconductor layer.

B9
Sub
C
31. (Amended) An active matrix display device comprising:
a substrate having an insulating surface;
a plurality of pixel electrodes arranged in a matrix form over said substrate;
a plurality of switching elements operationally connected to said pixel electrodes, each of said switching elements comprising a thin film transistor;
a display medium capable of electrically changing luminous strength disposed at each of said pixel electrodes; and
a CMOS circuit comprising at least one n-channel thin film transistor and one p-channel thin film transistor,

wherein each of the film transistors of the switching elements and said n-channel and p-channel thin film transistors comprises a crystalline semiconductor layer, a gate insulating film adjacent to said crystalline semiconductor layer and a gate electrode adjacent to said gate insulating film.

*B9
cond.*

32. (Amended) The active matrix display device according to claim 31 wherein said substrate is a glass substrate.

33. (Amended) The active matrix display device according to claim 31 wherein said gate electrode is located over said semiconductor layer.

Subj

35. (Amended) An active matrix display device comprising:
a substrate having an insulating surface;
a plurality of pixel electrodes arranged in a matrix form over said substrate;
a plurality of switching elements operationally connected to said pixel electrodes, each of said switching elements comprising a thin film transistor;
a display medium capable of electrically changing luminous strength disposed at each of said pixel electrodes; and
a CMOS circuit comprising at least one n-channel thin film transistor and one p-channel thin film transistor,

B10
wherein each of the film transistors of the switching elements and said n-channel and p-channel thin film transistors comprises a crystalline semiconductor layer, a gate insulating film adjacent to said crystalline semiconductor layer and a gate electrode adjacent to said gate insulating film, and said crystalline semiconductor layer has source and drain regions and at least one lightly doped region.

36. (Amended) The active matrix display device according to claim 35 wherein said substrate is a glass substrate.